Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	(programmable adj pin adj assignment) and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/30 08:42
S1	9832	(pld or fpga or cpld) same (component or cell or element or device) same (contact\$3 or conduct\$5 or lead\$3 or connect\$3 or interconnect\$3 or "i/o" or (input adj output) pin or ball or column)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/21 09:57
S2	29	(pld or fpga or cpld) same (component or cell or element or device) same (contact\$3 or conduct\$5 or lead\$3 or connect\$3 or interconnect\$3 or "i/o" or (input adj output) pin or ball or column) same multilayer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/15 16:24
S3	58	(pld or fpga or cpld) same (component or cell or element or device) same (contact\$3 or conduct\$5 or lead\$3 or connect\$3 or interconnect\$3 or "i/o" or (input adj output) pin or ball or column) same (multilayer or (plurality near3 layer))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/15 16:24
S4	9882	(pld or fpga or cpld) same (component or cell or element or device) same (contact\$3 or conduct\$5 or lead\$3 or connect\$3 or interconnect\$3 or "i/o" or (input adj output) or pin or ball or column)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/21 09:57
S5	9875	(pld or fpga or cpld) same (component or cell or element or device) same (contact\$3 or conduct\$3 or lead\$3 or connect\$3 or interconnect\$3 or "i/o" or (input adj output) or pin or ball or column)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/21 09:58
S6	769	(pld or fpga or cpld) same (component or cell or element or device) same (contact\$3 or conduct\$3 or lead\$3 or connect\$3 or interconnect\$3 or "i/o" or (input adj output) or pin or ball or column) and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/21 09:59
S7	214	(pld or fpga or cpld) same (component or cell or element or device) same (contact\$3 or conduct\$3 or lead\$3 or connect\$3 or interconnect\$3 or "i/o" or (input adj output) or pin or ball or column) and rout\$3 and layer and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/22 15:54

S8	84	(pld or fpga or cpld) same (component or cell or element or device) same (contact\$3 or conduct\$3 or lead\$3 or connect\$3 or interconnect\$3 or "i/o" or (input adj output) or pin or ball or column) and rout\$3 and layer and channel and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/21 10:00
S9	12	(pld or fpga or cpld) same (component or cell or element or device) same (contact\$3 or conduct\$3 or lead\$3 or connect\$3 or interconnect\$3 or "i/o" or (input adj output) or pin or ball or column) same assign\$5 and rout\$3 and layer and channel and "716"/\$. ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/21 10:05
S10	27	(pld or fpga or cpld) same (component or cell or element or device) same (contact\$3 or conduct\$3 or lead\$3 or connect\$3 or interconnect\$3 or "i/o" or (input adj output) or pin or ball or column) same (assign\$5 or map\$4) and rout\$3 and layer and channel and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/21 10:45
S11	29	(pld or fpga or cpld) same (component or cell or element or device) same (contact\$3 or conduct\$3 or lead\$3 or connect\$3 or interconnect\$3 or "i/o" or (input adj output) or pin or ball or column) same (assign\$5 or map\$4) and layer and channel and "716"/\$. ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/21 11:03
S12	2	"6388890".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/21 10:56
S13	3	(pld or fpga or cpld) same (component or cell or element or device) same (contact\$3 or conduct\$3 or lead\$3 or connect\$3 or interconnect\$3 or "i/o" or (input adj output) or pin or ball or column) same (assign\$5 or map\$4) same layer same channel	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/21 11:02
S14	20	((pld or fpga or cpld) near3 (component or cell or element or device or asic)) same (contact\$3 or conduct\$3 or lead\$3 or connect\$3 or interconnect\$3 or "i/o" or (input adj output) or pin or ball or column) same (assign\$5 or map\$4) and layer and channel and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/21 11:06

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S15	20	((pld or fpga or cpld) near3 (component or cell or element or device or asic)) same (contact\$3 or conduct\$3 or lead\$3 or connect\$3 or interconnect\$3 or "i/o" or (input adj output) or pin or ball or column) same (assign\$5 or map\$4) and layer and channel and ("716"/\$ "174"/\$ "29"/\$). ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/21 11:31
S16	31	((pld or fpga or cpld) near3 (component or cell or element or device or asic)) same (contact\$3 or conduct\$3 or lead\$3 or connect\$3 or interconnect\$3) same ("i/o" or (input adj output) or pin or ball or column) same (assign\$5 or map\$4) and ("716"/\$ "174"/\$ "29"/\$).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/21 11:32
S17	18	((pld or fpga or cpld) near3 (component or cell or element or device or asic)) same (contact\$3 or conduct\$3 or lead\$3 or connect\$3 or interconnect\$3) same ("i/o" or (input adj output) or pin or ball or column) same (assign\$5 or map\$4) and (optomiz\$4 or improv\$3) and ("716"/\$ "174"/\$ "29"/\$).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/21 11:33
S18	23	((pld or fpga or cpld) near3 (component or cell or element or device or asic)) same (contact\$3 or conduct\$3 or lead\$3 or connect\$3 or interconnect\$3) same ("i/o" or (input adj output) or pin or ball or column) same (assign\$5 or map\$4) and (optimiz\$4 or improv\$3) and ("716"/\$ "174"/\$ "29"/\$).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/21 11:33
S19	3	((pld or fpga or cpld) near3 (component or cell or element or device or asic)) same (contact\$3 or conduct\$3 or lead\$3 or connect\$3 or interconnect\$3) same ("i/o" or (input adj output) or pin or ball or column) same (assign\$5 or map\$4) same (optimiz\$4 or improv\$3) and ("716"/\$ "174"/\$ "29"/\$).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/21 11:56

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S20	3	((pld or fpga or cpld) near3 (component or cell or element or device or asic)) same (contact\$3 or conduct\$3 or lead\$3 or connect\$3 or interconnect\$3) same ("i/o" or (input adj output) or pin or ball or column or matrix) same (assign\$5 or map\$4) same (optimiz\$4 or improv\$3) and ("716"/\$ "174"/\$ "29"/\$).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/21 11:59
S21	256	((pld or fpga or cpld) near3 (component or cell or element or device or asic)) same (contact\$3 or conduct\$3 or lead\$3 or connect\$3 or interconnect\$3) same ("i/o" or (input adj output) or pin or ball or column or matrix) and ("716"/\$ "174"/\$ "29"/\$). ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/21 13:46
S22	60	((pld or fpga or cpld) near3 (component or cell or element or device or asic) near3 (contact\$3 or conduct\$3 or lead\$3 or connect\$3 or interconnect\$3)) same ("i/o" or (input adj output) or pin or ball or column or matrix) and ("716"/\$ "174"/\$ "29"/\$). ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/21 12:02
S23	48	((pld or fpga or cpld) near3 (contact\$3 or conduct\$3 or lead\$3 or connect\$3 or interconnect\$3) near3 (component or cell or element or device or asic) ) same ("i/o" or (input adj output) or pin or ball or column or matrix) and ("716"/\$ "174"/\$ "29"/\$).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/21 12:57
S24	0	(signal near3 rout\$3 near3 device) same (pld or fpga or cpld) same channel same layer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/21 12:59
S25	4	(signal near3 rout\$3 near3 device) same (pld or fpga or cpld) same channel	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/21 13:05
S26	74	(signal near3 rout\$3 near3 device) same (pld or fpga or cpld)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/21 13:06

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S27	20299	(signal adj rout\$3 adh device) same (pld or fpga or cpld)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/21 13:06
S28	2	(signal adj rout\$3 adj device) same (pld or fpga or cpld)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/22 16:02
S29	1	((pld or fpga or cpld) near3 (component or cell or element or device or asic)) same (contact\$3 or conduct\$3 or lead\$3 or connect\$3 or interconnect\$3) same ("i/o" or (input adj output) or pin or ball or column or matrix) and ("716"/\$ "174"/\$ "29"/\$). ccls. and kida.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/25 10:01
S30	30	("5006820" "5281151" "5451721" "5544018" "5686764" "6194668" "6198635" "6271478" "6310398" "6335493" "6388890" "6407343" "6452262" "6521846" "6545876").pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/21 13:48
S31	1	"6087732".PN.	USPAT; USOCR	OR	ON	2006/02/21 14:31
S32	1	"6225702".PN.	USPAT; USOCR	OR	ON	2006/02/21 14:32
S33	153	(pld or fpga or cpld) same (component or cell or element or device) same (contact\$3 or conduct\$3 or lead\$3 or connect\$3 or interconnect\$3 or "i/o" or (input adj output) or pin or ball or column) and rout\$3 and layer and footprint	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/22 16:05
S34	3	(signal adj rout\$3 adj device) and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/22 16:02
S35	6	(pld or fpga or cpld) same (component or cell or element or device) same (contact\$3 or conduct\$3 or lead\$3 or connect\$3 or interconnect\$3 or "i/o" or (input adj output) or pin or ball or column) same footprint and rout\$3 and layer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/22 16:13

S36	71	(pld or fpga or cpld) with (component or cell or element or device) with (contact\$3 or conduct\$3 or lead\$3 or connect\$3 or interconnect\$3 or "i/o" or (input adj output) or pin or ball or column) and footprint and rout\$3 and layer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/22 16:16
S37	58	(pld or fpga or cpld) with (component or cell or element or device) with (contact\$3 or conduct\$3 or lead\$3 or connect\$3 or interconnect\$3 or "i/o" or (input adj output) or pin or ball or column) and trac\$3 and footprint and rout\$3 and layer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/22 16:18
S38	13	(pld or fpga or cpld) with (component or cell or element or device) with (contact\$3 or conduct\$3 or lead\$3 or connect\$3 or interconnect\$3 or "i/o" or (input adj output) or pin or ball or column) and trac\$3 and footprint and rout\$3 and layer and ("716"/\$.ccls. "257"/\$.ccls. "438"/\$.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 08:53
S39	0	(pld or fpga or cpld) with (component or cell or element or device) with (contact\$3 or conduct\$3 or lead\$3 or connect\$3 or interconnect\$3 or "i/o" or (input adj output) or pin or ball or column) with (breakout or breakingout or (break\$3 adj out)) and trac\$3 and footprint and rout\$3 and layer and ("716"/\$.ccls. "257"/\$.ccls. "438"/\$. ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 08:54
S40	0	(pld or fpga or cpld) with (component or cell or element or device) with (contact\$3 or conduct\$3 or lead\$3 or connect\$3 or interconnect\$3 or "i/o" or (input adj output) or pin or ball or column) same (breakout or breakingout or (break\$3 adj out)) and trac\$3 and footprint and rout\$3 and layer and ("716"/\$.ccls. "257"/\$.ccls. "438"/\$.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 08:55
S41	0	(pld or fpga or cpld) with (component or cell or element or device) with (contact\$3 or conduct\$3 or lead\$3 or connect\$3 or interconnect\$3 or "i/o" or (input adj output) or pin or ball or column) and (breakout or breakingout or (break\$3 adj out)) and trac\$3 and footprint and rout\$3 and layer and ("716"/\$.ccls. "257"/\$.ccls. "438"/\$.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 08:57

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S42	0	(pld or fpga or cpld) same (component or cell or element or device) same (contact\$3 or conduct\$3 or lead\$3 or connect\$3 or interconnect\$3 or "i/o" or (input adj output) or pin or ball or column) and (breakout or breakingout or (break\$3 adj out)) and trac\$3 and footprint and rout\$3 and layer and ("716"/\$.ccls. "257"/\$.ccls. "438"/\$.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 08:57
S43	59	(pld or fpga or cpld) same (component or cell or element or device) same (contact\$3 or conduct\$3 or lead\$3 or connect\$3 or interconnect\$3 or "i/o" or (input adj output) or pin or ball or column) and (breakout or breakingout or (break\$3 adj out))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 08:58
S44	25	(pld or fpga or cpld) same (component or cell or element or device) same (contact\$3 or conduct\$3 or lead\$3 or connect\$3 or interconnect\$3 or "i/o" or (input adj output) or pin or ball or column) and (breakout or breakingout or (break\$3 adj out)) and layer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 11:13
S45	1	(pld or fpga or cpld) same (component or cell or element or device) same (contact\$3 or conduct\$3 or lead\$3 or connect\$3 or interconnect\$3 or "i/o" or (input adj output) or pin or ball or column) and (breakout or breakingout or (break\$3 adj out)) and layer and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 09:07
S46	0	(pld or fpga or cpld) same (component or cell or element or device) same (contact\$3 or conduct\$3 or lead\$3 or connect\$3 or interconnect\$3 or "i/o" or (input adj output) or pin or ball or column) and ((breakout or breakingout or (break\$3 adj out)) near\$3 trac\$3) and layer and "716"/\$. ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 09:06
S47	0	(pld or fpga or cpld) same (component or cell or element or device) same (contact\$3 or conduct\$3 or lead\$3 or connect\$3 or interconnect\$3 or "i/o" or (input adj output) or pin or ball or column) and ((breakout or breakingout or (break\$3 adj out)) near3 trac\$3) and layer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 09:06

S48	6182119	(TECHNIQUE FOR ACCOMMODATING ELECTRONIC COMPONENTS ON A MULTILAYER SIGNAL ROUTING DEVICE).ti.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM TDB	OR	ON	2006/05/23 10:02
S49	0	(TECHNIQUE adj FOR adj ACCOMMODATING adj ELECTRONIC adj COMPONENTS adj ON adj A adj MULTILAYER adj SIGNAL adj ROUTING adj DEVICE).ti.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 10:03
S50	28	(SIGNAL adj ROUTING adj DEVICE).ti.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 10:10
S51	2367052	WO 2004/047508.ptpn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 10:10
S52	0	WO2004/047508.ptpn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 10:11
S53	0	WO04/047508.ptpn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 10:11
S54	0	WO04/47508.ptpn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 10:11
S55	1012	(pld or fpga or cpld) same (contact\$3 or conduct\$3 or lead\$3 or connect\$3 or interconnect\$3 or "i/o" or (input adjoutput) or pin or ball or column) and (breakout or breakingout or (break\$3 adj out) or break\$3) and layer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 11:14
S56	131	(pld or fpga or cpld) same (contact\$3 or conduct\$3 or lead\$3 or connect\$3 or interconnect\$3 or "i/o" or (input adjoutput) or pin or ball or column) and (breakout or breakingout or (break\$3 adj out) or break\$3) same channel and layer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 11:15

S57	2	(pld or fpga or cpld) same (contact\$3 or conduct\$3 or lead\$3 or connect\$3 or interconnect\$3 or "i/o" or (input adjoutput) or pin or ball or column) and (breakout or breakingout or (break\$3 adj out) or break\$3) same channel same trac\$3 and layer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 14:22
S58	1588	pcb with (contact\$3 or conduct\$3 or lead\$3 or connect\$3 or interconnect\$3 or "i/o" or (input adj output) or pin or ball or column) same channel	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 11:28
S59	0	pcb with (contact\$3 or conduct\$3 or lead\$3 or connect\$3 or interconnect\$3 or "i/o" or (input adj output) or pin or ball or column) same channel same trac43 same assign\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 11:28
S60	0	pcb with (contact\$3 or conduct\$3 or lead\$3 or connect\$3 or interconnect\$3 or "i/o" or (input adj output) or pin or ball or column) same channel same trac43 and (assign\$3 or map\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 11:29
S61	542	pcb with (contact\$3 or conduct\$3 or lead\$3 or connect\$3 or interconnect\$3 or "i/o" or (input adj output) or pin or ball or column) same trac\$3 and (assign\$3 or map\$4) and channel	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 11:30
S62	100	pcb with (contact\$3 or conduct\$3 or lead\$3 or connect\$3 or interconnect\$3 or "i/o" or (input adj output) or pin or ball or column) same trac\$3 and (assign\$3 or map\$4) and channel and (PLD or FPGA or CPLD)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 11:30
S63	8	pcb with (contact\$3 or conduct\$3 or lead\$3 or connect\$3 or interconnect\$3 or "i/o" or (input adj output) or pin or ball or column) same trac\$3 same channel and (assign\$3 or map\$4) and (PLD or FPGA or CPLD)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 11:33
S64	81	pcb with (contact\$3 or conduct\$3 or lead\$3 or connect\$3 or interconnect\$3 or "i/o" or (input adj output) or pin or ball or column) same trac\$3 same channel and (assign\$3 or map\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 12:13
S65	5720	(pattern with electrically with conductive trac\$3) with pcb	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 11:45

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S66	20	pattern with electrically with conductive with trac\$3 with pcb	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 11:47
S67	0	pattern with electrically with conductive with trac\$3 with pcb same channel	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 11:47
S68	3	pattern with electrically with conductive with trac\$3 with pcb and channel	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 11:47
S69	45	pcb with (contact\$3 or conduct\$3 or lead\$3 or connect\$3 or interconnect\$3 or "i/o" or (input adj output) or pin or ball or column) with channel same trac\$3 and (assign\$3 or map\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 12:14
S70	0	(pin near3 assign\$4) with (PCB or (signal adj routing adj device)) same trac\$3 same channel	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 14:00
S71	0	(pin near3 (assign\$4 or map)) with (PCB or (signal adj routing adj device)) same trac\$3 same channel	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 14:00
S72	2	(pin near3 (assign\$4 or map)) same (PCB or (signal adj routing adj device)) same trac\$3 same channel	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 14:04
S73	5	(pin near3 (assign\$4 or map)) same (PCB or (signal adj routing adj device)) same trac\$3 and channel	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 14:04
S74	368	(pld or fpga or cpld) same (pcb or (signal adj routing adj device))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/25 09:32
S75	190	(pld or fpga or cpld) with (pcb or (signal adj routing adj device))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 14:23

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S76	56	(pld or fpga or cpld) with (pcb or (signal adj routing adj device)) and layer and channel	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/24 10:29
S78	7	(pld or fpga or cpld) with (pcb or (signal adj routing adj device)) and layer and channel and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/24 10:30
S79	7	(pld or fpga or cpld) with (pcb or (signal adj routing adj device)) and layer and channel and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/24 10:41
S80	2	(pld or fpga or cpld) same (pcb or (signal adj routing adj device)) and layer and (channel with rout\$3) and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/24 10:49
S81	24	(pld or fpga or cpld) same layer and (channel with rout\$3) and "716"/\$. ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/24 11:56
S82	25	(pld or fpga or cpld or (programmable adj device)) same layer and (channel with rout\$3) and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/24 11:58
S83	2	(pld or fpga or cpld or (programmable adj device)) same layer and (channel with rout\$3) and "716"/\$.ccls. and @pd <"20000619"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/24 12:19
S84	1	(pld or fpga or cpld or (programmable adj device)) same (channel with rout\$3) and layer and "716"/\$.ccls. and @pd <"20000619"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/24 12:55
S85	8	(pld or fpga or cpld or (programmable adj device)) near3 (channel with rout\$3) and @pd <"20000619"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/24 13:04
S86	7	(pld or fpga or cpld or (programmable adj device)) near3 (channel near3 rout\$3) and @pd <"20000619"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/24 12:55

S87	132	(pld or fpga or cpld or (programmable adj device)) same (connect43 or interconnect\$3 or coupl\$3 or attach\$3 or bond\$3 or join\$3) and (channel with rout\$3) and @pd <"20000619"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/24 13:05
S88	202	(pld or fpga or cpld or (programmable adj device)) same (connect\$3 or interconnect\$3 or coupl\$3 or attach\$3 or bond\$3 or join\$3) and (channel with rout\$3) and @pd <"20000619"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/24 13:06
S89	13	(pld or fpga or cpld or (programmable adj device)) same (connect\$3 or interconnect\$3 or coupl\$3 or attach\$3 or bond\$3 or join\$3) and (channel with rout\$3) and @pd <"20000619" and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/24 13:36
S90	0	(pld or fpga or cpld or (programmable adj device)) same (assign\$3 near3 (pin or contact or input or output or i/o)) same (channel with rout\$3) and @pd <"20000619" and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/24 13:37
S91	2	(pld or fpga or cpld or (programmable adj device)) same (assign\$3 or assignment or map\$3) same (pin or contact or input or output or i/o) same (channel with rout\$3) and @pd <"20000619" and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/24 13:38
S92	2	((pld or fpga or cpld) with (pcb or (signal adj routing adj device))) and channel and (pattern near3 trac\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/25 09:37
S93	16	((pld or fpga or cpld) with (pcb or (signal adj routing adj device))) and channel and ((pattern near3 trac\$3) or (pin near3 assign\$5))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/25 09:37
S94	23	((pld or fpga or cpld) with (pcb or (signal adj routing adj device))) and channel and ((pattern near3 trac\$3) or (pin near3 (assign\$5 or map\$3)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/25 09:48
S95	31	((pld or fpga or cpld) same (pcb or (signal adj routing adj device))) and channel and ((pattern near3 trac\$3) or (pin near3 (assign\$5 or map\$3)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/25 09:48

S96	69	((pld or fpga or cpld) near3 (pcb or (signal adj routing adj device)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/25 09:49
S97	30	((pld or fpga or cpld) near3 (pcb or (signal adj routing adj device))) and channel	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/25 09:49
S98	14	(signal adj routing adj device) with channel	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/25 10:04
S99	0	(signal adj routing adj device) with channel and (pld or fpga or cpld or (programmable near3 device))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/25 10:05
S10 0	109	((signal adj routing adj device) or pcb) with channel and (pld or fpga or cpld or (programmable near3 device))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/25 10:05
S10 1	3	((signal adj routing adj device) or pcb) with channel with (pld or fpga or cpld or (programmable near3 device))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/25 10:19
S10 2	19	((signal adj routing adj device) or pcb) with (pld or fpga or cpld or (programmable near3 device)) and channel and pattern and ((pin or i/o or contact or ball or column or input or output or input/output) near3 (assign\$5 or mapr))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/25 10:21
\$10 3	19	((signal adj routing adj device) or pcb) with (pld or fpga or cpld or (programmable near3 device)) and channel and pattern and ((pin or i/o or contact or ball or column or input or output or input/output) near3 (assign\$5 or mapr)) and trac\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/25 11:55

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S10 4	2	((signal adj routing adj device) or pcb) with (pld or fpga or cpld or (programmable near3 device)) and channel and pattern and ((pin or i/o or contact or ball or column or input or output or input/output) near3 (assign\$5 or map\$3) near3 program\$4) and trac\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/25 14:48
S10 5	1	lie.in. and pld and pcb and rout\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/25 14:50
S10 6	1	lie.in. and pld and ((pin or contact or i/o or terminal) near3 (map\$3 or assign\$5)) and rout\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/25 14:51
S10 7	1	lie.in. and pld and ((pin or contact or i/o or terminal) near3 (map\$3 or assign\$5))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/25 14:52
S10 8	4	lie.in. and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/25 15:00
S10 9	1	"20030014620".PN.	US-PGPUB	OR	ON	2006/05/25 14:57
S11 0	1	"6763506".PN.	USPAT; USOCR	OR	ON	2006/05/25 14:58
S11 1	1	"20020015401".PN.	US-PGPUB	OR	ON	2006/05/25 14:59
S11 2	134	(pld or fpga or cpld) and pcb and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/25 15:00
S11 3	14	((pld or fpga or cpld) same (contact or pin or terminal or i/o or input or output) near3 (map\$3 or assign\$5)) and pcb and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/25 15:03
S11 4	0	((pld or fpga or cpld) same (contact or pin or terminal or i/o or input or output) near3 (map\$3 or assign\$5)) and (conductive near3 trace) and pcb and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/25 15:03

S11 5	5	((pld or fpga or cpld) same (contact or pin or terminal or i/o or input or output) near3 (map\$3 or assign\$5)) and trace\$3 and pcb and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/25 15:04
S11 6	5	((pld or fpga or cpld) same (contact or pin or terminal or i/o or input or output) near3 (map\$3 or assign\$5)) and trac\$3 and pcb and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/25 15:39
S11 7	6	((pld or fpga or cpld) same (contact or pin or terminal or i/o or input or output) near3 (map\$3 or assign\$5)) and (trac\$3 or channel) and pcb and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/30 08:41